Transform an LED Driver from Buck to Boost for Enhanced Flexibility, Reduced BOM

With a few extra components and some rearrangement of the topology, a buck-mode DC-DC converter IC can be made into a boost-mode device, to drive LED strings with voltages higher than the supply voltage.

By Fons Janssen, Principal Member Technical Staff, and Field Application Engineer, Maxim Integrated

The hysteretic-buck LED-driver is a popular, easily implemented current source for situations where the voltage across the LED string is lower than the input voltage. By rearranging the external components, it is practical to switch this topology from buck mode to boost mode, to support LED strings where the sum of the diode drops is greater than the input voltage.

While there are many boost regulators available, this topology allows a single buck regulator IC to provide both buck and boost functions, and so may simplify the bill of materials (BOM) and reduce overall cost. Although using the buck device for boost operation may result in increased variation in the LED current beyond what is acceptable, an additional control loop can be added to further regulate the current, if needed.

This transformation example uses the MAX16822/32 hysteretic buck converters from Maxim Integrated, which are 2-MHz high-brightness LED-driver ICs with integrated MOSFET and high-side current sense, Figure 1. (The MAX16822 and MAX16832 differ only in current rating: 500mA versus 1A, respectively.)

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Figure 1: Typical application circuit of the MAX16832 as a buck-converter LED driver

This circuit regulates the voltage on sense resistor Rsense so that a constant current flows through the LEDs that are in series with that resistor. The MOSFET within the MAX16832 is turned on for currents below the set point and turned off for currents above it. When the MOSFET is on, the current ramps up and flows from input voltage Vin to GND via the sense resistor, the LEDs, the inductor, and the MOSFET; when the MOSFET is off, the current ramps down and flows back to Vin via the sense resistor, the LEDs, the inductor, and diode D1.

Adding the hysteresis results in a self-oscillating system which generates a sawtooth-shaped LED current, Figure 2. The amplitude of the sawtooth is determined by the amount of hysteresis. Capacitor C3 acts as a filter, so that the LEDs will mainly see a DC current. This topology is known as a high-side buck topology.

Going from buck to boost

A buck topology can only be used if the voltage across the LEDs is less than the input voltage. When voltage across the LEDs is greater than the input voltage, a boost topology is needed. Since the boost topology also has the switching MOSFET on the low-side, it is straightforward to change the high-side buck topology into a boost topology by rearranging the external components, Figure 3. In this boost topology, the current is regulated in the same way as in the high-side buck topology.

The difference is that the LEDs are no longer in series with the sense resistor and inductor. The result is that the input current is regulated rather than the LED current. Figure 4 shows the waveforms for the input and output currents; the LED current is a filtered version of the output current via C3.
The result of this arrangement is that the LED current will depend not only on the regulated input current (IIN), but also on input voltage (VIN), output voltage (VLED), and the efficiency (η) of the converter:

\[ I_{LED} = \frac{V_{IN} \cdot I_{LED}}{V_{LED}} \]

Figure 3: Change topology from high-side buck to boost just requires some rearrangement of the external components.

If the resulting variation in the LED current is greater than acceptable, an extra circuit based on the MAX8515 (a wide-input, 0.6V shunt regulator for isolated DC-to-DC converters) can be added to regulate the LED current, Figure 5.

The difference between both currents is integrated by capacitor C2. If the MAX8515 sinks more current than the TEMP_I pin sources, the voltage decreases; the reverse is true as well. The set point for the input current IIN is proportional to this voltage, Figure 6. Therefore, if VFB is smaller than the 0.6V reference, no current will be sunk and the voltage on TEMP_I increases. This, in turn, will increase the input power, and therefore the LED current and VFB. If VFB is greater than the reference, the voltage on TEMP_I will be pulled lower in order to reduce the LED current.

The MAX8515 acts as an error amplifier and compares feedback voltage VFB to an internal reference voltage of 0.6V. VFB is directly proportional to the LED current, with VFB = R2 × ILED. Since the output of the amplifier can sink current from the TEMP_I pin but cannot source current, a small constant current is sourced by the TEMP_I pin itself.

Figure 6: The MAX8515 manages the sinking and sourcing, as seen in the relation between voltage on TEMP_I and input-current set point.
LED current-control loop minimizes variations
These parameters apply to the control loop used to regulate the LED current, Figure 7:
- The 0.6V reference voltage of the MAX8515 is the input for the control loop; $V_{FB}$ is the output and is directly proportional to the LED current, with $I_{LED} = \frac{V_{FB}}{R_2}$;
- G1 is the gain of the MAX8515 and resistor R2 (note that the gain of MAX8515 is actually negative due to the inverting action of the NPN transistor; this is compensated by swapping the plus and minus signs on the adder);
- Capacitor C2 is the integrator while G2 is the gain between the TEMP_I voltage and the feedback voltage.

This control loop will regulate $V_{FB}$ to 0.6V:

$$I_{LED} = \frac{0.6V}{R_2}$$

To correctly configure the boost circuit, sense resistor $R_{SENSE}$ should be chosen so that the maximum input current is slightly higher than needed. The extra control loop will then reduce the input current to get the correct LED-current value. The value of this resistor can be calculated as follows:

$$R_{sense} < \frac{\eta \times V_{in} \times 200mV}{I_{LED}V_{LED}}$$

The additional sense resistor R2 can be calculated by:

$$R_2 = \frac{0.6V}{I_{LED}}$$

Overvoltage protection also needed
A LED normally fails as a short circuit, thus lowering the output voltage. If the output voltage remains higher than the input voltage, the circuit will continue to function correctly. However, if the LED fails by becoming a high impedance (open circuit) rather than a short circuit, the output current will charge the output capacitor C3 to a value beyond the operating range of the IC, and cause it to fail.

![Figure 1: Control loop for regulating the LED current begins by maintaining the feedback voltage $V_{FB}$ at 0.6V.](image)

To protect the circuit from such a condition, a few extra components can be added, Figure 8, to the basic circuit. If the gate voltage of Q2 reaches its turn-on threshold, Q2 will pull down the DIM pin on the converter. This will automatically stop the converter from switching and the output voltage will slowly drop until Q2 is turned off. The cycle will repeat so that the output voltage will vary around the overvoltage threshold, which is chosen to be within the operating range of the converter.

<table>
<thead>
<tr>
<th>Reference</th>
<th>Without LED current regulation</th>
<th>With LED current regulation</th>
</tr>
</thead>
<tbody>
<tr>
<td>L1</td>
<td>100µH</td>
<td>100µH</td>
</tr>
<tr>
<td>$R_{SENSE}$</td>
<td>470mΩ</td>
<td>300mΩ</td>
</tr>
<tr>
<td>R2</td>
<td>N.A.</td>
<td>3Ω</td>
</tr>
<tr>
<td>R3</td>
<td>N.A.</td>
<td>27kΩ</td>
</tr>
<tr>
<td>C1, C2</td>
<td>1µF</td>
<td>1µF</td>
</tr>
<tr>
<td>C3</td>
<td>10µF</td>
<td>10µF</td>
</tr>
</tbody>
</table>

Table 1: key component values

Measurements confirm, extend analysis
To verify the buck/boost analysis and assess overall performance, two circuits were built and tested, one with the extra LED current regulation and one without it. The circuits were designed to drive eight LEDs (+24V) at 200mA from a 12-V input. The efficiency was estimated to be around 96%.

With the output at 4.8W (24V x 200mA), input power was 4.8 W/0.95 ≈ 5.05 W. Using a 12-V power supply, the input current should be regulated to 5.05W/12V = 421 mA, which results in a 470-mΩ value for the sense resistor (200mV/421mA).

For the circuit with regulation of the LED current, R2 needs to be 3Ω (600mV/200mA). To extend the input voltage down to 8V, the sense resistor should meet the following condition:

$$R_{sense} < \frac{0.95 \times 8V \times 200mA}{200mA \times 24V} = 3.17mΩ$$

so a value of 300mΩ was chosen.

![Figure 9: LED current versus input voltage with (red) and without (blue) additional regulation shows the output current's sensitivity to the value the input voltage.](image)

To demonstrate the added value of the LED current regulation, the LED current was recorded for an input voltage range of 8V up to 16V for both circuits, Figure 9. It is clear that for the circuit without LED current regulation, the LED current is only at its 200-mA target value.
when the input voltage is at its nominal value of 12V. For other values, it scales linearly with the input voltage. If the input voltage is regulated, the variation on VIN may be very small and result in an acceptable LED current variation.

In comparison, the circuit with LED current regulation does not show this effect but has a constant value over the entire input voltage range. The extra control loop clearly shows its value by regulating the LED current to the target value for the entire input-voltage range; it is slightly lower only with 8-V input. Most likely, the efficiency was slightly lower than the estimated 95% due to losses in R2. A quick measurement showed that the input current was at the maximum for VIN = 8V. A simple fix would be to lower RSENSE to 270mΩ.

Another nice feature of the hysteretic-buck LED driver is that the control loop is inherently stable, since there is no feedback. Adding the additional control loop introduces feedback, which could introduce instabilities. A Bode plot of the stability of the control loop revealed that the circuit has a phase margin of about 47°, which is sufficient to guarantee stable operation, Figure 10.

![Bode plot of the LED driver circuit with the current regulation confirms the circuit has sufficient phase margin to guarantee stable operation.](image)

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**Figure 10:** The Bode plot of the LED driver circuit with the current regulation confirms the circuit has sufficient phase margin to guarantee stable operation.

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**References**


**About the author**

Fons Janssen is a Principal Member of Technical Staff for Maxim Integrated. Prior to joining the company in 2003, he worked at ThreeFive Photonics developing integrated optical circuits, and before that at Lucent Technologies, where he worked on optical-access networks. He graduated from Eindhoven University of Technology (The Netherlands) with an Electrical Engineering degree. Postgraduate studies at this university led to a Master’s degree in Technological Design.

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